F I G. 8

PRIOR ART

POSITIVE POWER
SOURCE V_{DD}

50

51

CONTROL
SIGNAL V_{IN}

58

COMMON ELECTRODE
SIGNAL V_{COM}

(TO LIQUID CRYSTAL PANEL)

57

52

55

56

53

54

GND

F I G. 9

PRIOR ART

62

REFERENCE
VOLTAGE
GENERATOR CIRCUIT

CONTROL
CIRCUIT

61

SOURCE DRIVER

VIDEO DATA

S(1) S(2) · · · · · · S(i) · · · · · · S(N)

G(1)

G(2)

63

GATE DRIVER

G(i)

G(M)

50

COMMON ELECTRODE
SIGNAL GENERATOR
CIRCUIT

ADJUSTMENT VOLTAGE

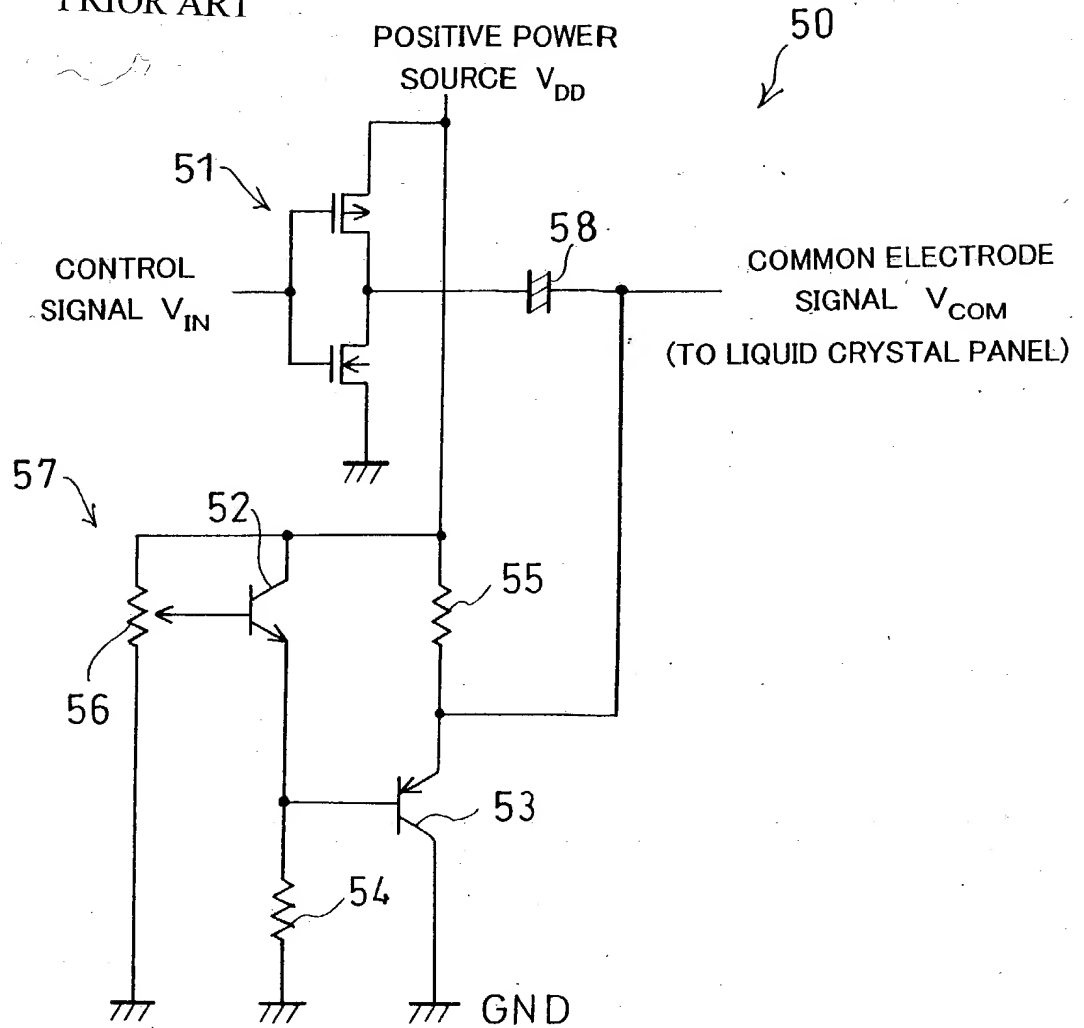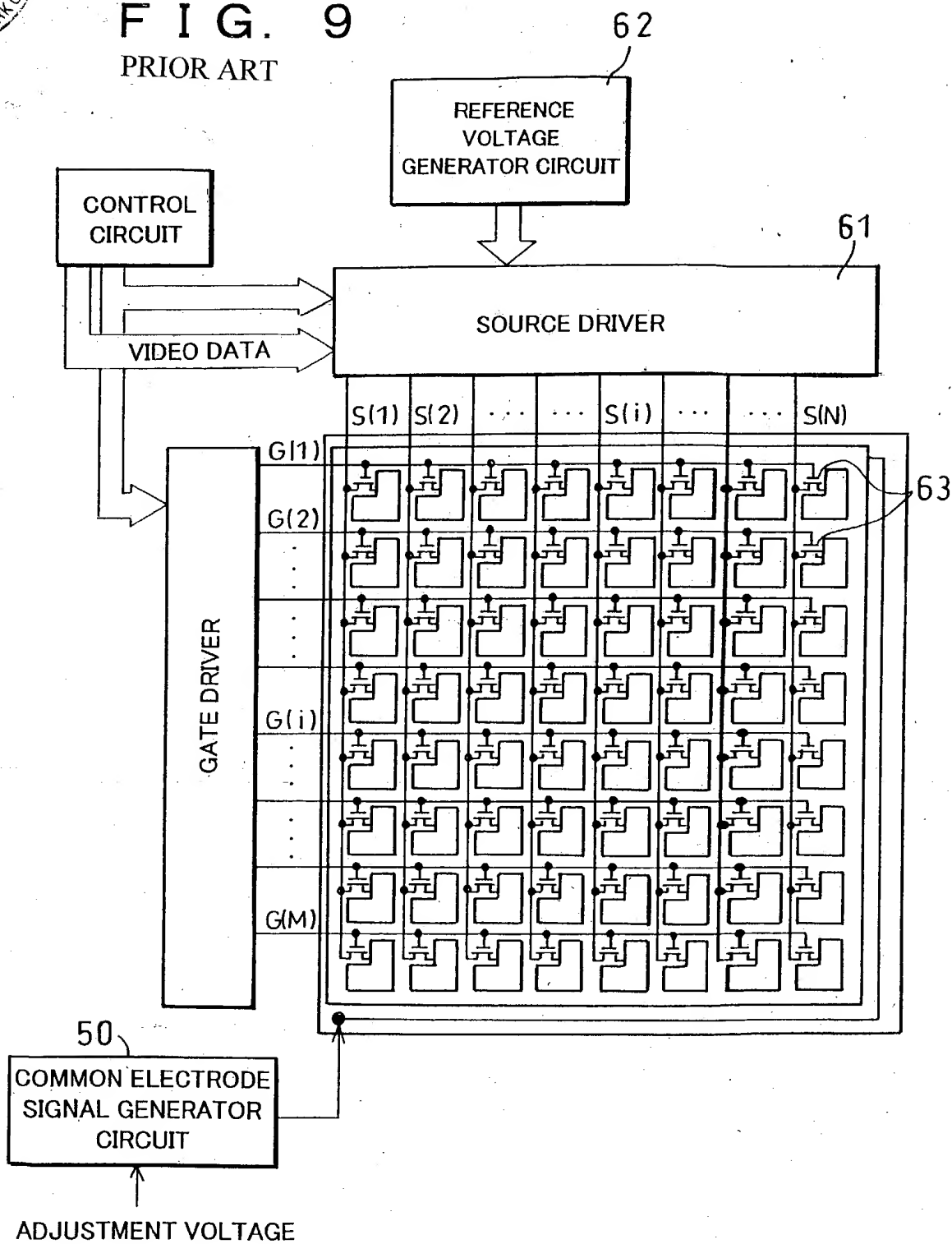# F I G. 1 0

PRIOR ART

HIGH REFERENCE VOLTAGE V_{HIGH}

62

REFERENCE VOLTAGE (TO SOURCE DRIVER)

R11   R21

SW1   Amp1   Amp   V1

R12   R22

SW3   SW2   Amp2   Amp   V2

R13   R23

SW5   SW4   Amp3   Amp   V3

R14

R24   SW7   SW6   Amp4   Amp   V4

R15   R25

SW8

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